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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10612061
	Filing Date		2003-06-30
	First Named Inventor	Eric L. Debes	
	Art Unit		2193
	Examiner Name	Malzahn, David H.	
	Attorney Docket Number		42P15765

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	3	5497497		2008-01-23	Miller, et al.	
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	4	KAWAKAMI, Y., ET AL., "A Single-Chip Digital Signal Processor for Voiceband Applications,"IEEE, 1980 International Solid-State Circuits Conference, pp. 40-41	☐
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6	CASE, B., "Philips Hopes to Displace DSPs with VLIW, TriMedia Processors Aimed at Future Multimedia Embedded Apps," Microprocessor Report, December 1994, pp. 12-18	☐
7	Gwennap, L., "New PA-RISC Processor Decodes MPEG Video, H"s PA-7100LC Uses New Instructions to Eliminate Decoder Chip," Microprocessor Report, January, 1994, pp. 16-17	☐
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12	MARGULIS, N., "i860 Microprocessor Architecture," McGraw Hill, Inc., 1990, Chapters 6, 7, 8, 10, and 11	☐
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15	MOTOROLA, INC., "Errata to MC88110 Second Generation RISC Microprocessor User's Manual," 1992, pp. 1-11	☐
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17	SHIPNES, J., "Graphics Processing with the 88110 RISC Microprocessor," Motorola, Inc. IEEE, No 0-8186-26455-0/92, 1992, pp. 169-174	☐
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20	LEVINTHAL, ET AL., "Chap–A SIMD Graphics Processor," Computer Graphics Project, ACM, vol. 18, No. 3, July 1984, pp. 77-81	☐
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